EL7242, EL7252



Data Sheet

January 1996, Rev. B

FN7285

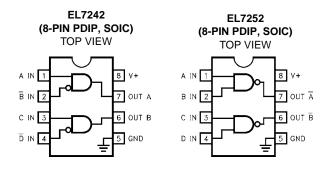
Dual Input, High Speed, Dual Channel Power MOSFET Driver



The EL7242/EL7252 dual input, 2channel drivers achieve the same excellent switching performance of the

EL7212 family while providing added flexibility. The 2-input logic and configuration is applicable to numerous power MOSFET drive circuits. As with other Elantec drivers, the EL7242/EL7252 are excellent for driving large capacitive loads with minimal delay and switching times. "Shoot-thru" protection and latching circuits can be implemented by simply "cross-coupling" the 2-channels.

Pinouts



Manufactured under U.S. Patent Nos. 5,334,883, #5,341,047

Features

- Logic AND/NAND input
- 3V and 5V Input compatible
- Clocking speeds up to 10MHz
- · 20ns Switching/delay time
- 2A Peak drive
- Isolated drains
- · Low output impedance
- Low quiescent current
- Wide operating voltage 4.5V16V

Applications

- Short circuit protected switching
- Under-voltage shut-down circuits
- · Switch-mode power supplies
- Motor controls
- Power MOSFET switching
- · Switching capacitive loads
- Shoot-thru protection
- Latching drivers

Ordering Information

PART NUMBER	TEMP. RANGE	PACKAGE	PKG. NO.
EL7242CN	-40°C to +85°C	8-Pin PDIP	MDP0031
EL7242CS	-40°C to +85°C	8-Pin SOIC	MDP0027
EL7252CN	-40°C to +85°C	8-Pin PDIP	MDP0031
EL7252CS	-40°C to +85°C	8-Pin SOIC	MDP0027

Absolute Maximum Ratings (T_A = 25°C)

Supply (V+ to Gnd) 16.5V	
Input Pins	
Combined Peak Output Current	
Storage Temperature Range65°C to +150°C	

Amb	bient Operating Temperature	40°C to +85°C
Ope	erating Junction Temperature	125°C
Pow	er Dissipation	
	SOIC	570mW
	PDIP	

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

IMPORTANT NOTE: All parameters having Min/Max specifications are guaranteed. Typical values are for information purposes only. Unless otherwise noted, all tests are at the specified temperature and are pulsed tests, therefore: $T_J = T_C = T_A$

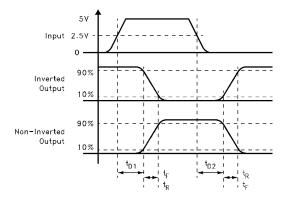
DC Electrical Specifications $T_A = 25^{\circ}C$, V = 15V unless otherwise specified

PARAMETER	DESCRIPTION	TEST CONDITIONS	MIN	ТҮР	МАХ	UNITS
INPUT					I.	1
V _{IH}	Logic "1' Input Voltage		2.4			V
IIH	Logic "1' Input Current	@V+		0.1	10	μA
VIL	Logic "0' Input Voltage				0.8	V
Ι _{ΙL}	Logic "0' Input Current	@0V		0.1	10	μA
V _{HVS}	Input Hysteresis			0.3		V
OUTPUT						
R _{OH}	Pull-Up Resistance	I _{OUT} = -100mA		3	6	Ω
R _{OL}	Pull-Down Resistance	I _{OUT} = +100mA		4	6	Ω
I _{PK}	Peak Output Current	Source Sink		2 2		A
IDC	Continuous Output Current	Source/Sink	100			mA
POWER SUPPL	Y			1		1
I _S	Power Supply Current	Inputs High		1	2.5	mA
VS	Operating Voltage		4.5		16	V

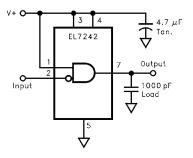
AC Electrical Specifications $T_A = 25^{\circ}C$, V = 15V unless otherwise specified

PARAMETER	DESCRIPTION	TEST CONDITIONS	MIN	TYP	MAX	UNITS	
SWITCHING CHARACTERISTICS							
t _R	Rise Time	C _L = 500pF C _L = 1000pF			10 20	ns	
t _F	Fall Time	C _L = 500pF C _L = 1000pF			10 20	ns	
t _{D-ON}	Turn-On Delay Time			20	25	ns	
^t D-OFF	Turn-Off Delay Time			20	25	ns	

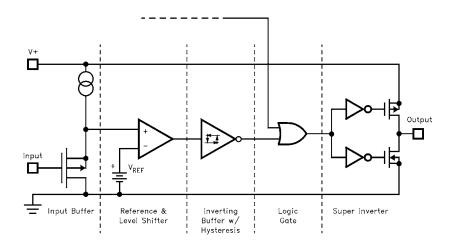
Timing Table



Standard Test Configuration



Simplified Schematic



Typical Performance Curves

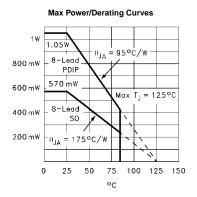
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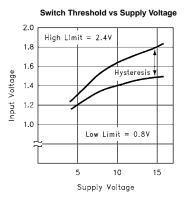
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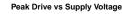
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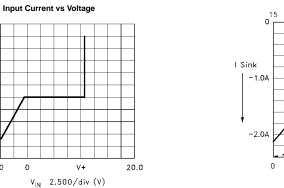
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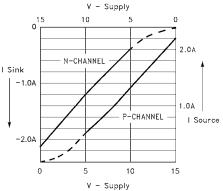
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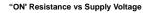


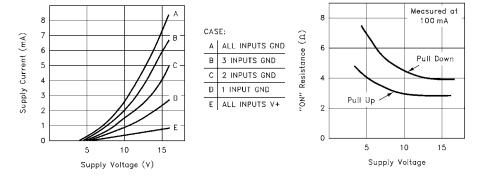




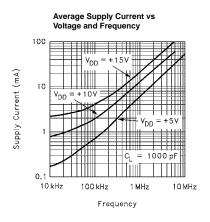
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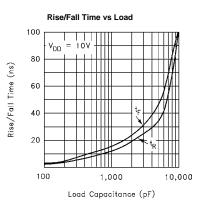


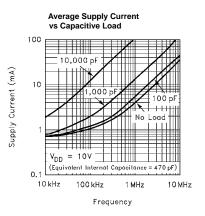


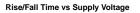


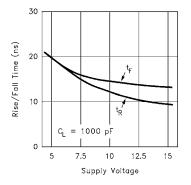
Typical Performance Curves (Continued)



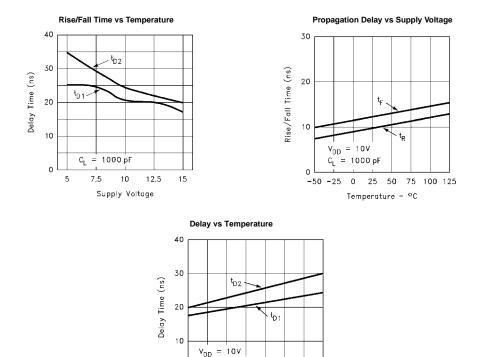








Typical Performance Curves (Continued)



-50 -25 0 25 50 75 100 125 Temperature - °C

= 1000 pF

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