# EL7242, EL7252



Data Sheet

#### January 1996, Rev. B

#### FN7285

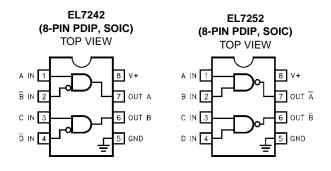
### Dual Input, High Speed, Dual Channel Power MOSFET Driver



The EL7242/EL7252 dual input, 2channel drivers achieve the same excellent switching performance of the

EL7212 family while providing added flexibility. The 2-input logic and configuration is applicable to numerous power MOSFET drive circuits. As with other Elantec drivers, the EL7242/EL7252 are excellent for driving large capacitive loads with minimal delay and switching times. "Shoot-thru" protection and latching circuits can be implemented by simply "cross-coupling" the 2-channels.

## **Pinouts**



Manufactured under U.S. Patent Nos. 5,334,883, #5,341,047

#### Features

- Logic AND/NAND input
- 3V and 5V Input compatible
- Clocking speeds up to 10MHz
- · 20ns Switching/delay time
- 2A Peak drive
- Isolated drains
- · Low output impedance
- Low quiescent current
- Wide operating voltage 4.5V16V

#### Applications

- Short circuit protected switching
- Under-voltage shut-down circuits
- · Switch-mode power supplies
- Motor controls
- Power MOSFET switching
- · Switching capacitive loads
- Shoot-thru protection
- Latching drivers

### **Ordering Information**

PART NUMBER	TEMP. RANGE	PACKAGE	PKG. NO.
EL7242CN	-40°C to +85°C	8-Pin PDIP	MDP0031
EL7242CS	-40°C to +85°C	8-Pin SOIC	MDP0027
EL7252CN	-40°C to +85°C	8-Pin PDIP	MDP0031
EL7252CS	-40°C to +85°C	8-Pin SOIC	MDP0027

#### Absolute Maximum Ratings (T<sub>A</sub> = 25°C)

Supply (V+ to Gnd) 16.5V	
Input Pins	
Combined Peak Output Current	
Storage Temperature Range65°C to +150°C	

Amb	bient Operating Temperature	40°C to +85°C
Ope	erating Junction Temperature	125°C
Pow	er Dissipation	
	SOIC	570mW
	PDIP	

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

IMPORTANT NOTE: All parameters having Min/Max specifications are guaranteed. Typical values are for information purposes only. Unless otherwise noted, all tests are at the specified temperature and are pulsed tests, therefore:  $T_J = T_C = T_A$ 

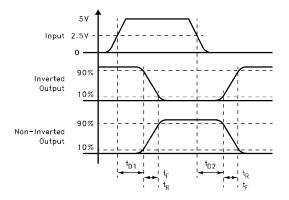
#### **DC Electrical Specifications** $T_A = 25^{\circ}C$ , V = 15V unless otherwise specified

PARAMETER	DESCRIPTION	TEST CONDITIONS	MIN	ТҮР	МАХ	UNITS
INPUT					I.	1
V <sub>IH</sub>	Logic "1' Input Voltage		2.4			V
IIH	Logic "1' Input Current	@V+		0.1	10	μA
VIL	Logic "0' Input Voltage				0.8	V
Ι <sub>ΙL</sub>	Logic "0' Input Current	@0V		0.1	10	μA
V <sub>HVS</sub>	Input Hysteresis			0.3		V
OUTPUT						
R <sub>OH</sub>	Pull-Up Resistance	I <sub>OUT</sub> = -100mA		3	6	Ω
R <sub>OL</sub>	Pull-Down Resistance	I <sub>OUT</sub> = +100mA		4	6	Ω
I <sub>PK</sub>	Peak Output Current	Source Sink		2 2		A
IDC	Continuous Output Current	Source/Sink	100			mA
POWER SUPPL	Y			1		1
I <sub>S</sub>	Power Supply Current	Inputs High		1	2.5	mA
VS	Operating Voltage		4.5		16	V

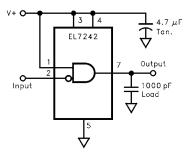
#### AC Electrical Specifications $T_A = 25^{\circ}C$ , V = 15V unless otherwise specified

PARAMETER	DESCRIPTION	TEST CONDITIONS	MIN	TYP	MAX	UNITS	
SWITCHING CHARACTERISTICS							
t <sub>R</sub>	Rise Time	C <sub>L</sub> = 500pF C <sub>L</sub> = 1000pF			10 20	ns	
t <sub>F</sub>	Fall Time	C <sub>L</sub> = 500pF C <sub>L</sub> = 1000pF			10 20	ns	
t <sub>D-ON</sub>	Turn-On Delay Time			20	25	ns	
<sup>t</sup> D-OFF	Turn-Off Delay Time			20	25	ns	

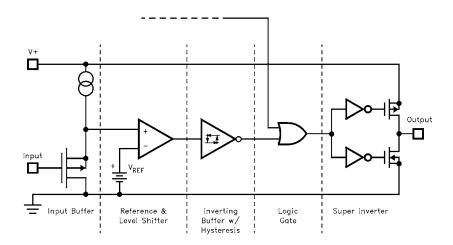
# Timing Table



## Standard Test Configuration



## **Simplified Schematic**



## **Typical Performance Curves**

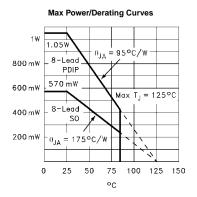
10.00

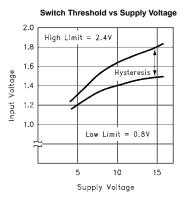
/div 0

-10.00

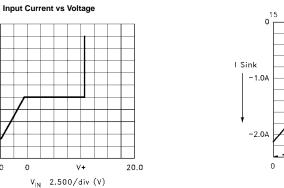
-5.0

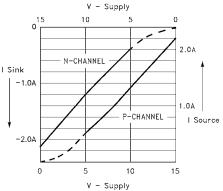
(MA) III 2.000







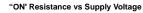


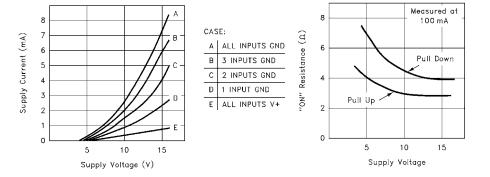




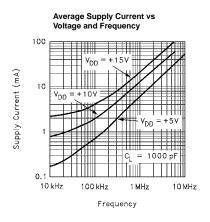
0

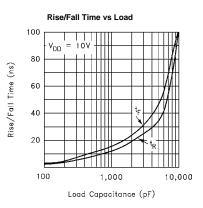


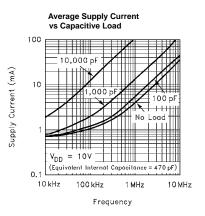


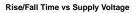


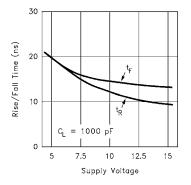
## Typical Performance Curves (Continued)



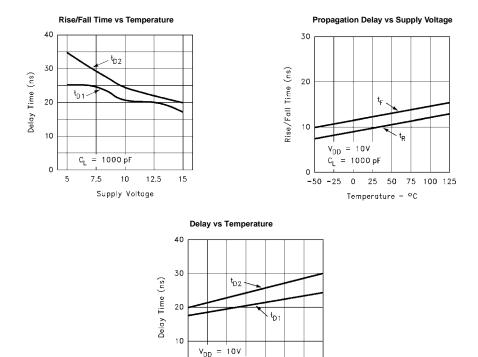








## Typical Performance Curves (Continued)



-50 -25 0 25 50 75 100 125 Temperature - °C

= 1000 pF

All Intersil U.S. products are manufactured, assembled and tested utilizing ISO9000 quality systems. Intersil Corporation's quality certifications can be viewed at www.intersil.com/design/quality

Intersil products are sold by description only. Intersil Corporation reserves the right to make changes in circuit design, software and/or specifications at any time without notice. Accordingly, the reader is cautioned to verify that data sheets are current before placing orders. Information furnished by Intersil is believed to be accurate and reliable. However, no responsibility is assumed by Intersil or its subsidiaries for its use; nor for any infringements of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Intersil or its subsidiaries.

For information regarding Intersil Corporation and its products, see www.intersil.com